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CIRCULAR

It is pleasure to inform that the Ministry of Electronics and Information Technology (MeiTY) under Chips to Startup (C2S) programme has granted Electronic Design and Automation (EDA) tools and advanced state of the art FPGA boards to the University College of Engineering (A), Osmania University. The key objective of the C2S programme is to make industry ready skilled manpower in VLSI domain.

The College is sharing the access of the EDA tools and FPGA hardware boards to all the faculty members, research scholars and students of the Osmania University. The EDA tools can be accessed by using Osmania University login credentials within the Campus LAN.

The utilization of these tools and hardware boards will be integral to your academic and professional growth.

EDA Tools:

1. Synopsys: VCS, Verdi, Spyglass, Design Compiler, IC Compiler and IC Validator
2. Cadence: Xcelium, Genus, Innovus, Tempus, Voltus, Quantus, Virtuoso, Assura, Spectre
3. Mentor Graphics: Questa, Precision RTL, Tessent, Nitro-Soc, Calibre, Tanner, Hyper lynx
4. ANSYS: Totem, Raptorx Galaxy
5. AMD Vivado
6. Keysight: ADS

FPGA Boards: Zynq, Pynq, Kria, Zed, Boolean, Urbana etc.

For obtaining login credentials and tools installation, contact:

- 1) Mr. Md Misbahuddin Assistant Professor, ECE Dept., UCE(A), OU Mobile: 9247184538
- 2) Mr. Md Toufeeq Ahmed (Research Scholar) Mobile: 7674845887
- 3) Ms. M Shivani (JRF-C2SProject) Mobile: 7330492722
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